

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	7	716/16.ccls. and FPGA and network and MAC	US-PGPUB; USPAT; USOCR	OR	OFF	2005/02/10 09:43
L2	5	L1 and @ad<"20010725"	US-PGPUB; USPAT; USOCR	OR	OFF	2005/02/10 09:44
L3	25	709/250.ccls. and FPGA and MAC	US-PGPUB; USPAT; USOCR	OR	OFF	2005/02/10 09:46
L4	12	L3 and @ad<"20010725"	US-PGPUB; USPAT; USOCR	OR	OFF	2005/02/10 09:44
L5	29	709/220,221,250.ccls. and FPGA and MAC	US-PGPUB; USPAT; USOCR	OR	OFF	2005/02/10 09:46
L6	16	L5 and @ad<"20010725"	US-PGPUB; USPAT; USOCR	OR	OFF	2005/02/10 09:46
S8	97	("3922889" "3783651" "6215327" "6215327" "6693452" "6781407" "6823502" "6272451" "5489856" "5874785" "4829203" "4866307" "4888498" "6020755" "4495622" "4590665" "4873459" "4910417" "4910508" "5197029" "5319261" "5521833" "5621650" "5625301" "5629876" "5640337" "5644496" "5898602" "4771285" "4862019" "4868426" "4868790" "4885719" "4918440" "5010522" "5015885" "5016217" "5200652" "5214327" "5225700" "5233206" "5286993" "5313119" "5325109" "5337254" "5339262" "5369377" "5384481" "5394103" "5408428").pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/09 12:53
S9	20	S8 and FPGA	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/09 13:03
S10	90	S8 and @ad<"20010725"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/09 12:54

S11	0	S10 and media adj independent adj interface	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/09 12:54
S12	0	S9 and MAC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/09 13:04
S13	10	S9 and network	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/09 13:06
S14	20990198	@ad<"20010725"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/09 13:06
S15	322682	S14 and integrated adj circuit	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/09 13:09
S16	132	IC and MAC and media adj independent adj interface	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/09 13:10
S17	2159	S15 and MAC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/09 13:10
S19	145	S17 and media adj independent adj interface	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/09 13:11
S20	92	S19 and PHY	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/09 13:14
S21	0	S20 and PHY adj module	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/09 13:12

S22	4	S20 and layer adj module	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/09 13:19
S23	21	S20 and FPGA	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/09 13:14
S24	2	MAC adj module and MII and PHY adj module	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/09 13:20
S25	0	S24 and @ad<"20010725"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/09 13:20
S26	79	media adj access adj control and media adj independent adj interface and integrated adj circuit and @ad<"20010725"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/09 13:21
S27	0	S26 and physical adj layer adj module	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/09 13:21
S28	71	S26 and physical adj layer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/09 13:21
S29	71	S28 and @ad<"20010725"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/09 13:22
S30	20	S29 and FPGA	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/09 13:31
S31	49	integrated adj circuit and MAC and FPGA and MII and PHY	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/09 13:32

S32	21	S31 and @ad<"20010725"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/09 13:34
S33	1	FPGA and network and MAC and MII and PHY and @ad<"20010725" and IC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/09 14:09
S34	2	"6080203".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/09 15:00
S35	0	physical adj layer near5 CLB and @ad<"20010725"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/09 15:01
S36	2	(physical adj layer) near5 (logic adj blocks)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/09 15:02
S37	1	S36 and @ad<"20010725"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/09 15:36
S38	11	logical adj control adj block	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/09 15:37
S39	0	FPGA and programmable adj logic adj fabric and configurable adj logic adj blocks and wireless and MAC and @ad<"20010725"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/09 15:38
S40	0	FPGA and programmable adj logic adj fabric and configurable adj logic adj blocks and wireless and @ad<"20010725"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/09 15:38
S41	0	FPGA and programmable adj logic adj fabric and configurable adj logic adj blocks and @ad<"20010725"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/09 15:41

S42	514	FPGA and configurable adj logic adj blocks and @ad<"20010725"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/09 15:42
S43	34	S42 and MAC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/09 15:49
S44	8	S43 and wireless	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/09 15:42
S45	0	configurable adj logic adj blocks near5 PHY and @ad<"20010725"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/09 15:50
S46	0	CLB near5 PHY and @ad<"20010725"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/09 15:52
S47	0	programmable adj logic adj block near5 PHY and @ad<"20010725"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/09 15:52
S48	0	programmable adj logic adj block near10 PHY and @ad<"20010725"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/09 16:31
S49	558	FPGA.ti. and @ad<"20010725"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/09 16:33
S50	5	FPGA.ti. and @ad<"20010725" and network and wireless	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/09 16:33
S51	5	("5684980" "5802290" "5838167" "6094063" "Re34363").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/02/10 09:42